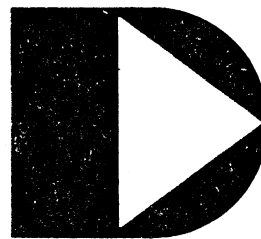


DATAPOINT 3800

APPLICATIONS PROCESSOR

ADDENDUM TO DATAPOINT 1800
FIRST LEVEL MAINTENANCE MANUAL

DATAPOINT CORPORATION



The leader in dispersed data processing™

Manual No. 75217-20

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SECTION 1

EQUIPMENT DESCRIPTION

1.1 INTRODUCTION

The information contained in this addendum is applicable only to the Datapoint 3800 Processor. Information common to both the Datapoint 1800 and 3800 Processors is contained in the basic Datapoint 1800 Processor Manual (Manual No. 75217-00). Only sections from the basic manual that have changes or additions are included in this addendum.

1.2 GENERAL

The 3800 Processor is similar to the 1800 Processor. The differences are listed below:

- a. The 3800 Processor can only be used as an ARC System applications processor. It does not support a diskette drive or serial communications.
- b. The processor does not have a communications I/O bus (J6 on rear of 1800). Also absent is (1) the communications logic on the display-I/O board and the supporting firmware on the processor-emulation board, (2) two protective ground leads from the communication bus connector J6 (on the display-I/O board) to the processor chassis, and (3) baud rate, receive/transmit clock source and NRZI jumpers on the display-I/O board.
- c. The processor does not have a micro bus I/O port (J7 on rear of 1800). Also absent is the micro bus logic on the display-I/O board and the supporting firmware on the processor-emulation board.
- d. The processor has only the 6600 external I/O bus (J1 on rear of 1800). Like the 1800 Processor, this bus does not supply power to peripheral equipment.
- e. The processor-emulation board has 8 PROMS for microcode (same board on 1800 has 16 PROMS). Also the I-Decode PROM's contain different firmware.

- f. The macro code in the 8K ROM PWA is changed to allow booting from the ARC System (Instead of from a disk drive). The macro code also includes a RIM buffer test. The 1800 diskette test has been deleted.

Figures 1-1 and 1-2 show the 3800 Processor.

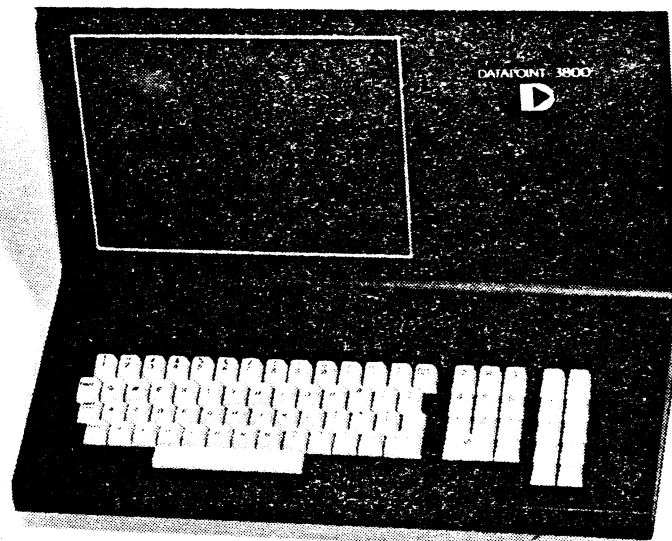


Figure 1-1. 3800 Processor - Keyboard Attached

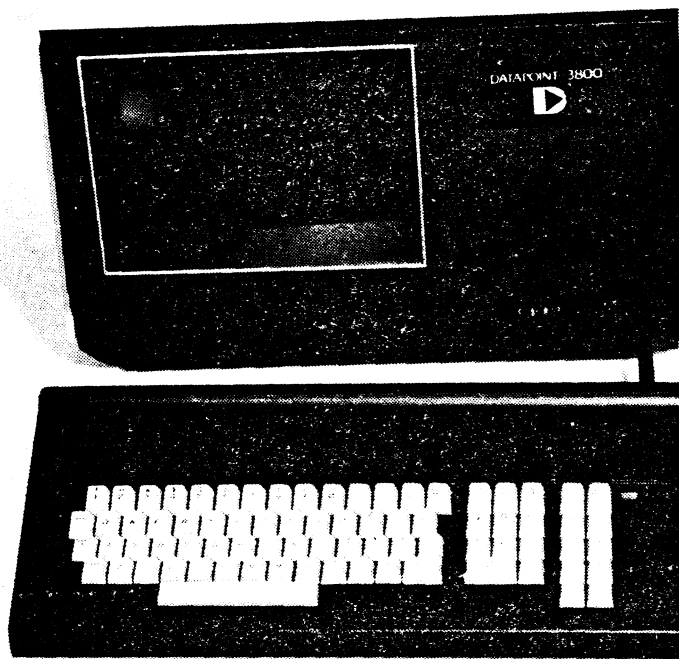


Figure 1-2. 3800 Processor - Keyboard Detached

SECTION 2

INSTALLATION

2.1 UNPACKING AND INSPECTION, AND INITIAL CHECKOUT

The unpacking and inspection, and initial checkout procedures contained in the basic manual are applicable to the 3800 Processor. References to the micro bus, communications bus, and setting of baud rate and clock jumpers should be overlooked.

2.2 INSTALLATION

Installation of the 3800 Processor requires that the processor be cabled to a RIM adapter and a suitable power source. The 3800 should be installed according to the following procedure:

- a. Place the processor in its operating location. Allow at least 6 inches clearance to gain access to the rear of the unit.
- b. Connect the I/O cable to J1 on the rear of the processor and to J1 on the RIM.
- c. Connect any additional I/O devices to connector J2 on the RIM. Maximum I/O cable length is 24 feet.
- d. Connect power cable to input power source.

2.3 SYSTEM CHECKOUT

- a. Position processor and RIM power switches to ON.
- b. Run applicable diagnostic or operational tests for the processor and attached RIM adapter.
- c. Run applicable diagnostic or operational tests for other attached peripheral equipment.
- d. Place all equipment in operational status.

SECTION 6

OPTIONS

6.1 GENERAL

Paragraphs 6.2.1, 6.2.2, and 6.2.3 in the basic manual do not apply to the 3800 Processor.

SECTION 7

PARTS LIST AND IPB'S

7.1 PARTS LIST

The parts contained in the basic manual are applicable to the 3800 Processor with changes and additions listed below. References refer to counterpart items in the basic manual.

<u>PART NUMBER</u>	<u>DESCRIPTION</u>	<u>REFERENCE DESIGNATION</u>
11-0522-006(9)	Assy, 3800 Terminal and Keyboard (Ref. 11-0522-001)	U1
10-1402-006(2)	Assy, 3800 Terminal (without Keyboard, Ref. 10-1402-001)	U1
10-1002-002(4)	PWA, 32K RAM I (Ref. 10-1002-001)	A8-A11
10-1166-007(2)	PWA, 8K ROM I (Ref. 10-1166-005)	A7
10-1340-002(3)	PWA, Display-I/O III (Ref. 10-1340-002)	A1
10-1342-004(5)	PWA, Processor-Emulation II (Ref. 10-1342-001)	A2

7.2 ILLUSTRATED PARTS BREAKDOWN

The assembly drawings contained in the basic manual are applicable to the 3800 Processor.

SECTION 8

REFERENCE MATERIAL

8.1 3800 PROCESSOR ROM DEBUG GUIDE

The 3800 Processor ROM Debug Program is the same as the 1800 Processor ROM Debug Program except that the "H" command performs a different function. Instead of initiating a diskette drive test, the "H" command initiates a RIM buffer test. The number preceding the "H" command must be a number from 1 to 6 (not 12345 as in the 1800 ROM Guide). This causes RIM addresses 0234, 0232, 0231, 0254, 0252, or 0251 to be used, respectively.

8.2 REFERENCE DRAWINGS

The reference drawings contained in the basic manual are applicable to the 3800 Processor with changes and additions listed below. References refer to counterpart drawing in the basic manual.

<u>FIGURE NUMBER</u>	<u>DRAWING NUMBER</u>	<u>DRAWING TITLE</u>	<u>REFERENCE DESIGNATOR</u>
8-1	01-0700	ICD, 3800 Processor (Ref. 01-0701)	
8-2	10-1340	PWA, Display-I/O III (Ref. 10-1340; for schematic diagram, see 01-0570 in the basic manual)	A1

The following drawings in the basic manual are not applicable to the 3800 Processor:

10-1509	CA, 1800 to Synchronous Modem and ACU
10-1510	CA, 1800 to Async Modem w/Rev. Ch.
10-1511	CA, 1800 to Sync Modem
10-1512	CA, 1800 to Async Modem and ACU

